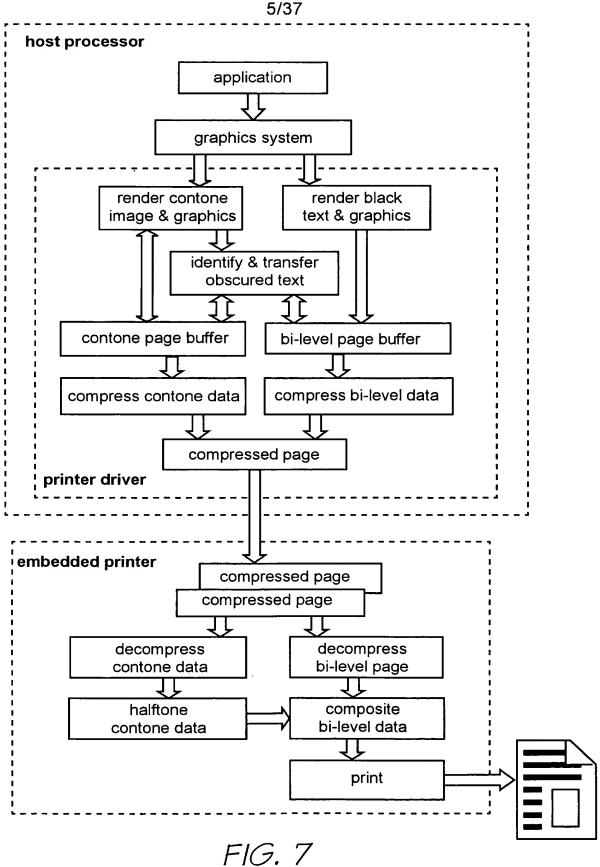


FIG. 5

		print page n+2	
	transfer page n+2	print page n+1	
render page n+2	transfer page n+1	print page n	
render page n+1	transfer page n		
render page n		•	

first page: ≤ 6 seconds	sustained: 2 seconds
2 seconds	2 seconds
≤ 2 seconds	
< 2 seconds	

FIG. 0



E.3 E.3 W

6/37

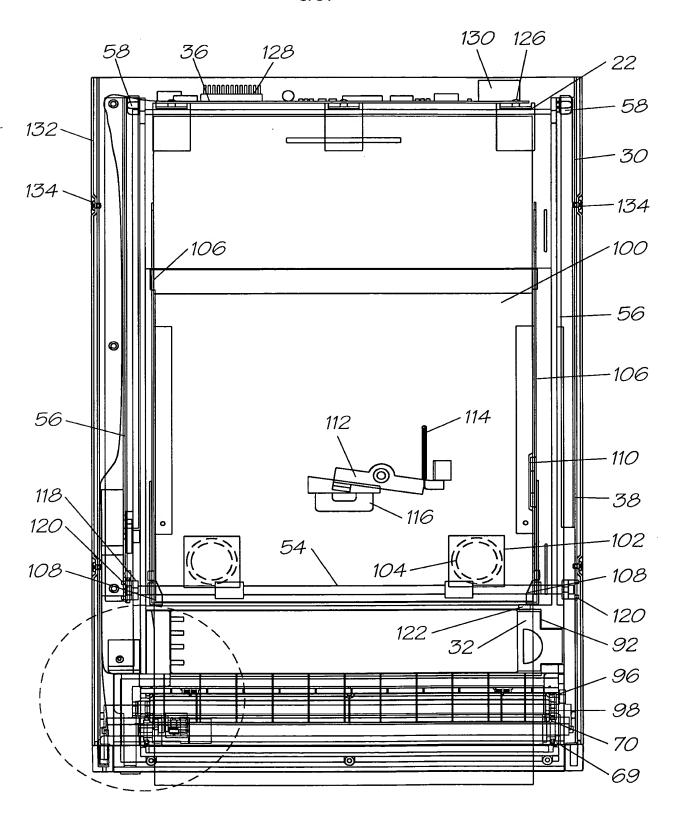
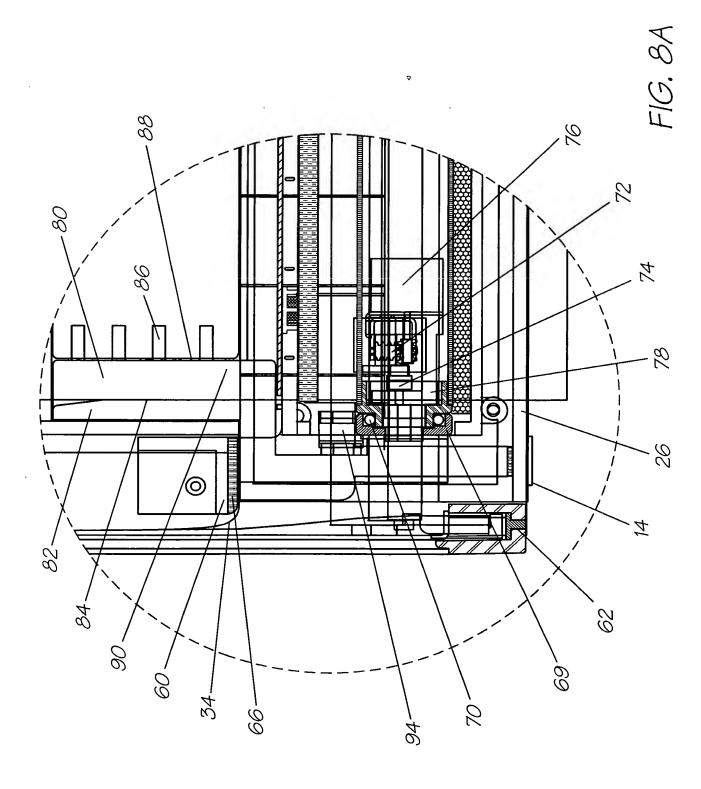
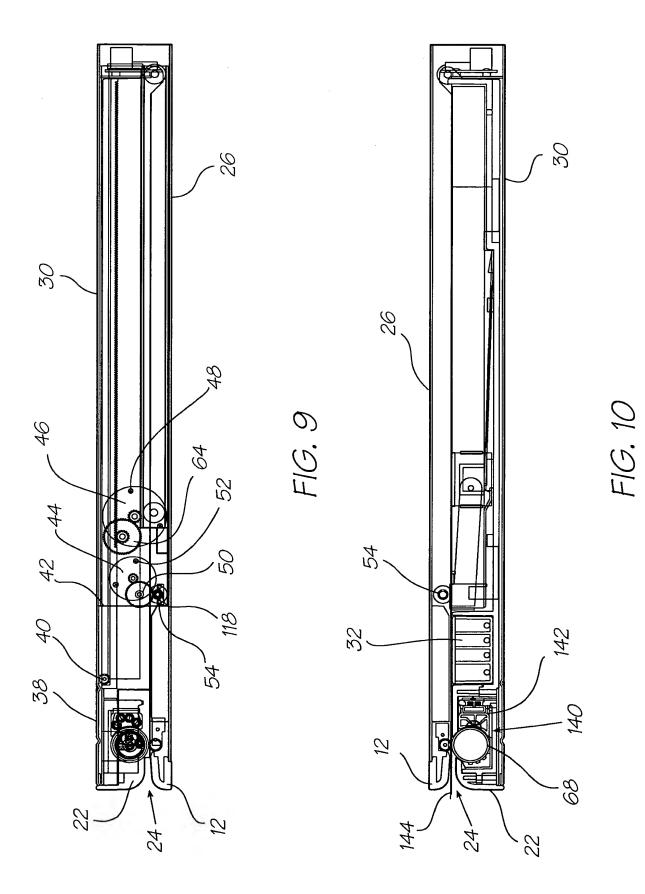
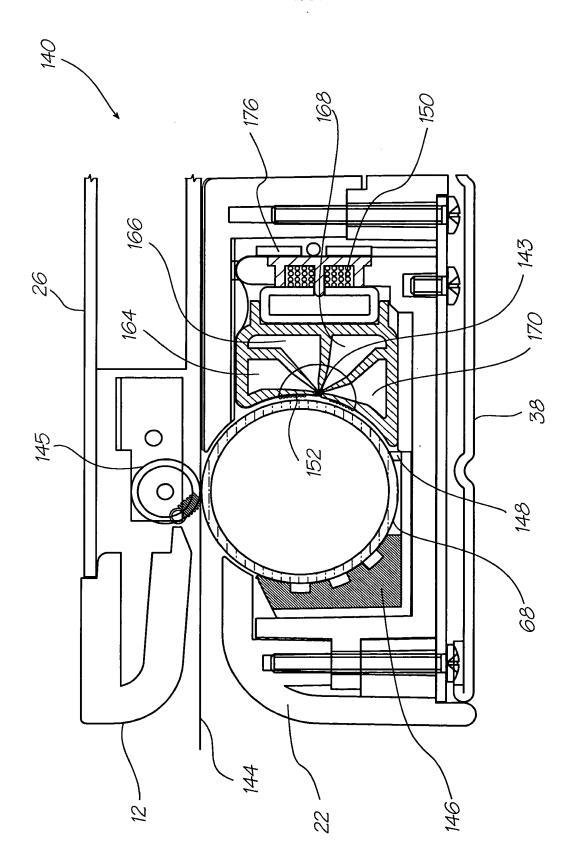


FIG. 8



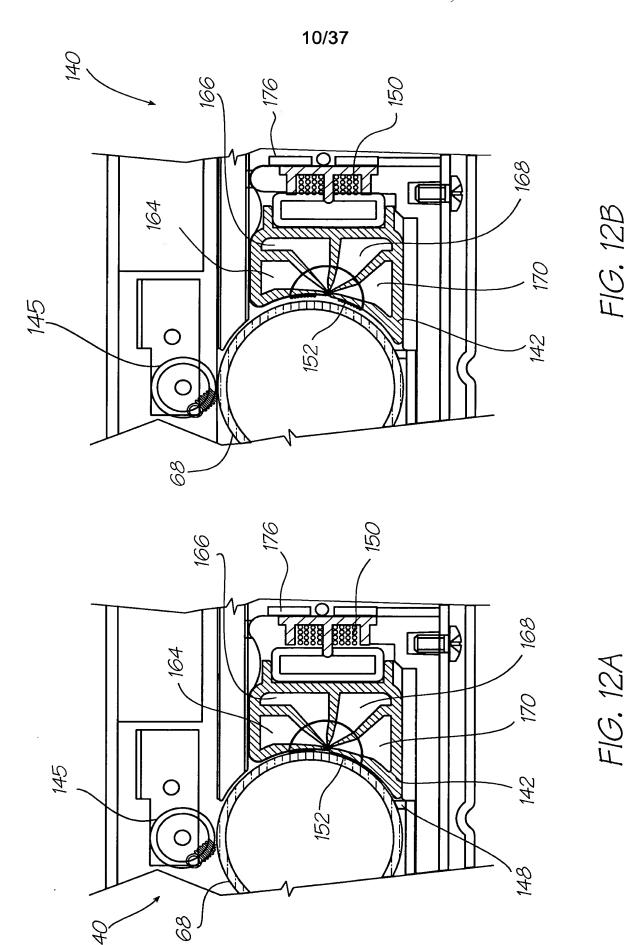
The hard of the transport of the hard of t



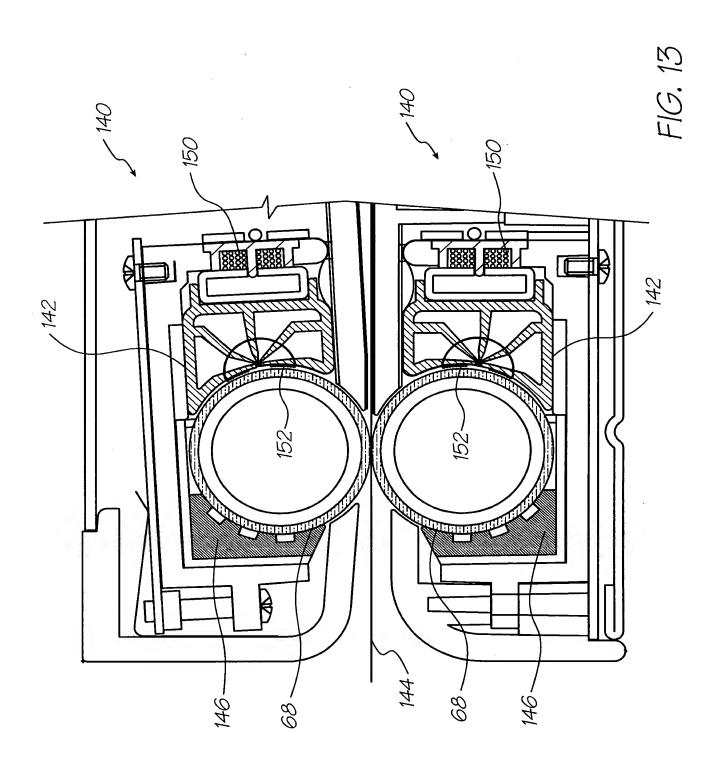


F1G. 11





A. B. W. Word when small strength and the strength of the stre



12/37

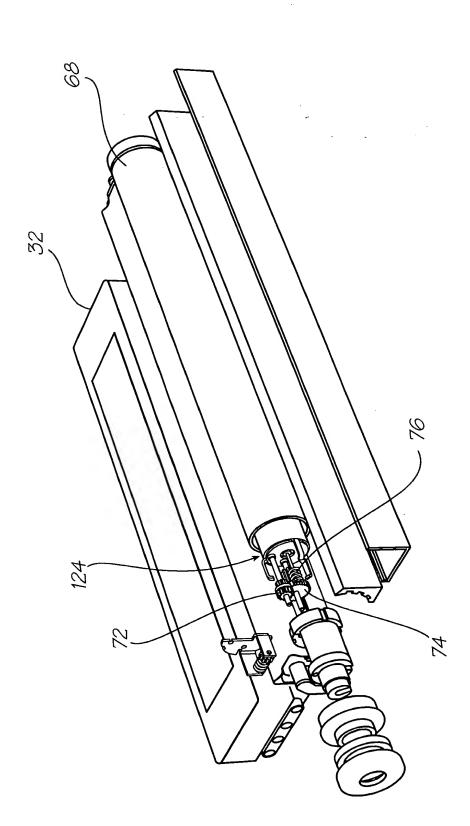
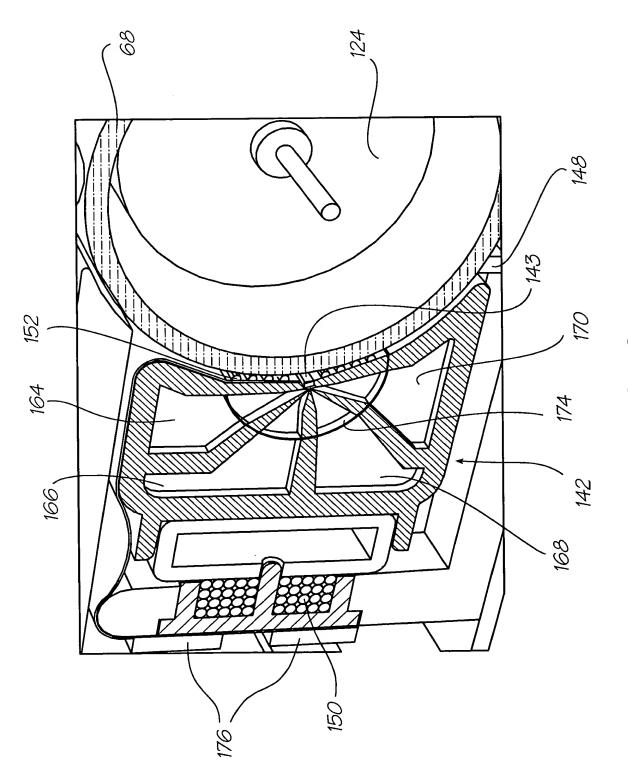


FIG. 14

FIG. 15



E. R. B. B. B. B. W. B. B. R. R. R. R. R. R. B. R. B.

FIG. 16

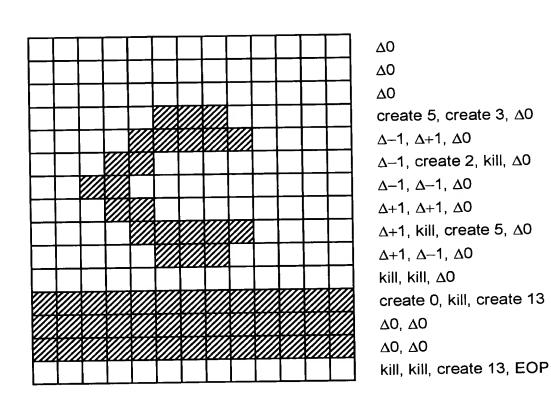


FIG. 17

LARLE DE DE DE MINI DE RABERTORA DE LA RESERVE

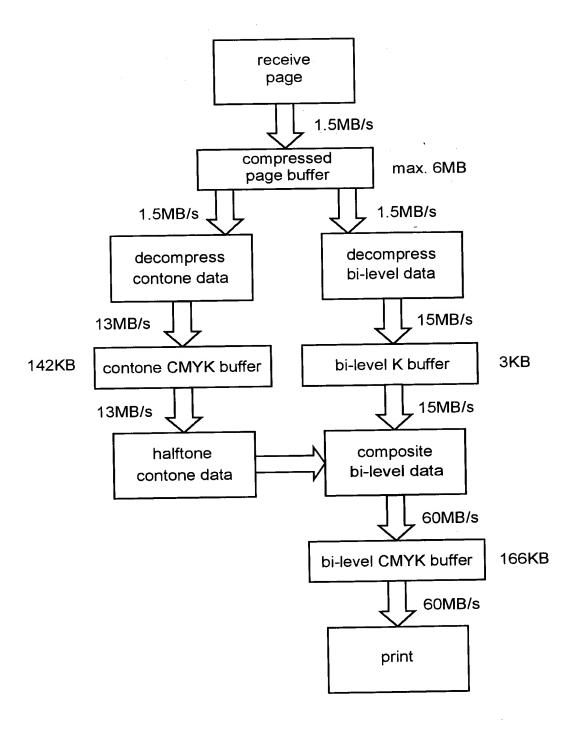
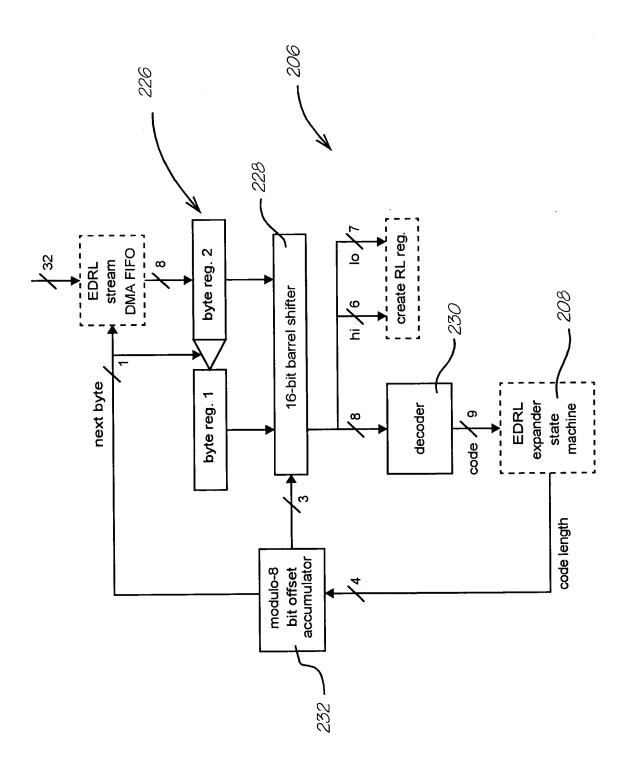


FIG. 19

18/37 188 pixel DMA FIFO line buffer prev.line buffer line buffer -202 \mathcal{w} runlength decoder runlength decoder runlength encoder scale factor reg page width reg. 206 edge register create RL reg. <del>1</del>3 EDRL -stream decoder 208 ref. edge reg. control register EDRL expander state machine delta 3 start 216 EOL

FIG. 20



F1G. 21

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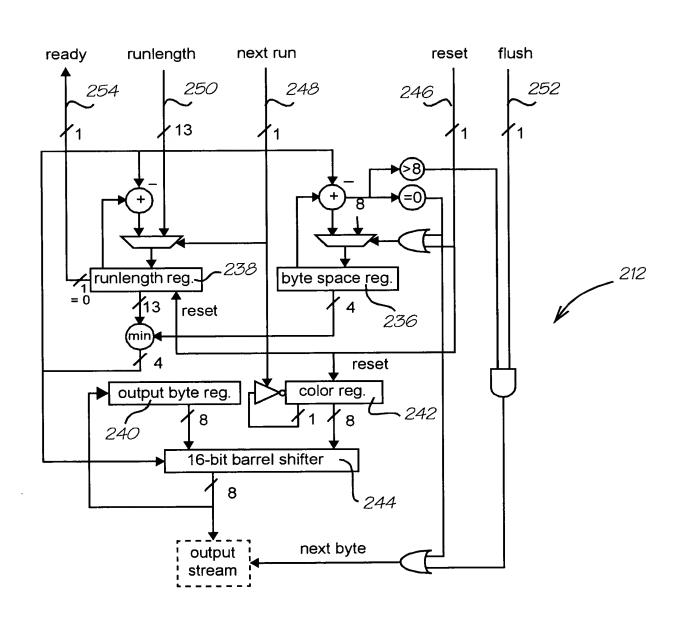
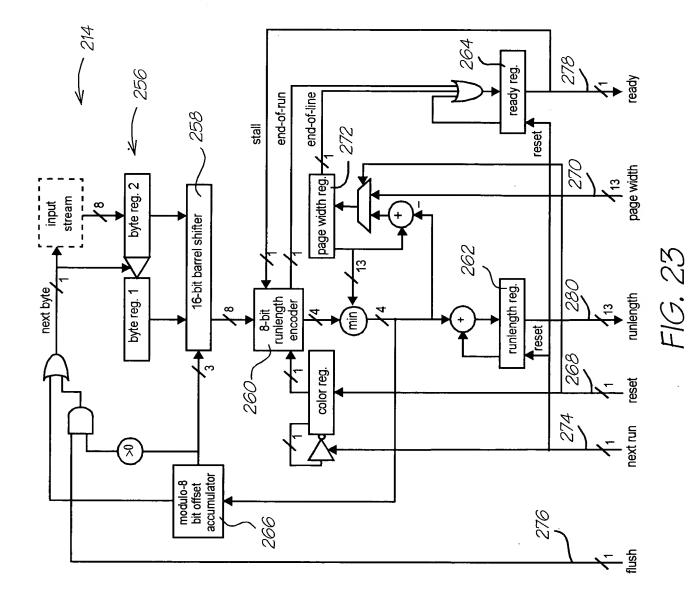


FIG. 22



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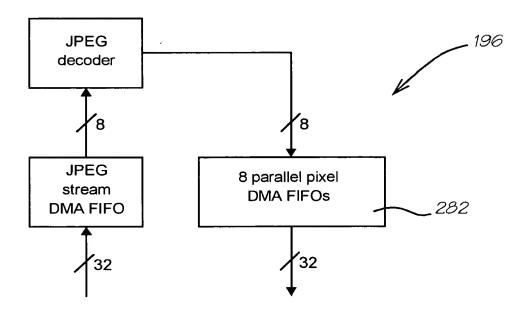


FIG. 24

target left margin	target page width	target right margin	
	black page width	i	
	contone page width	i	
page width (of pr	inthead)		

FIG. 26

LIT REAL THE LEASE TO THE REAL PROPERTY.

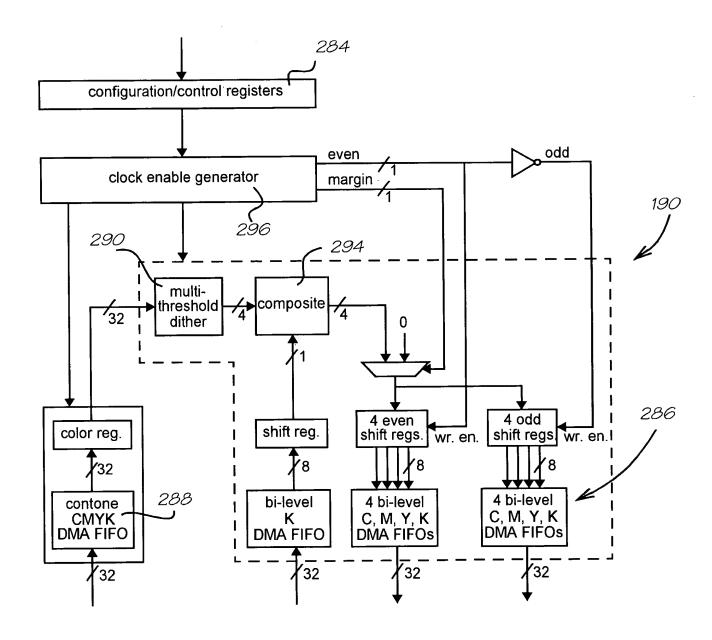


FIG. 25

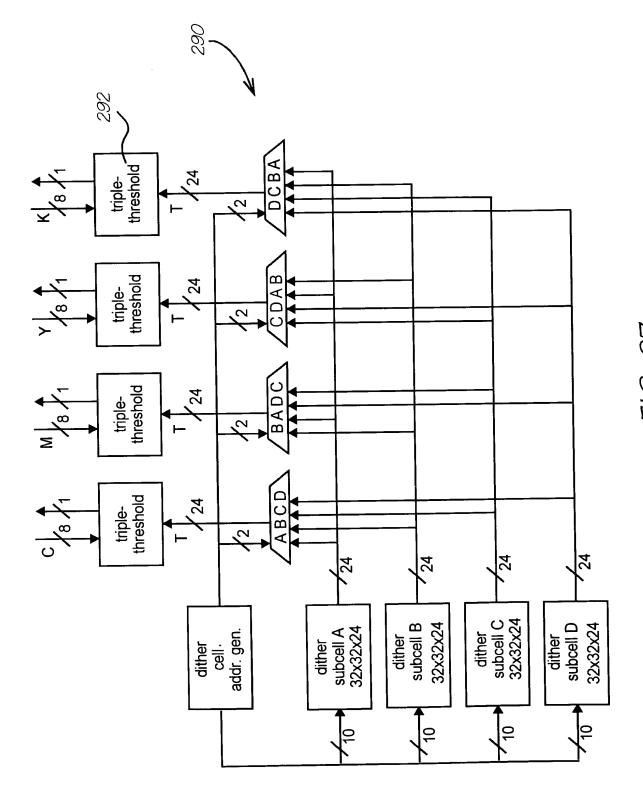


FIG. 27

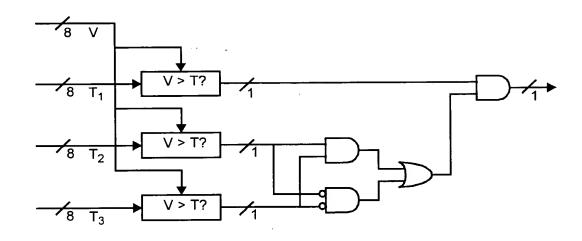


FIG. 28

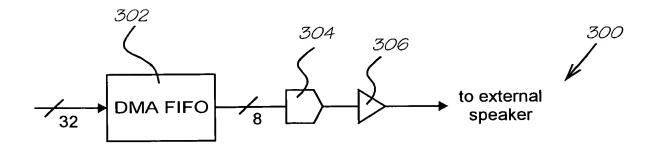
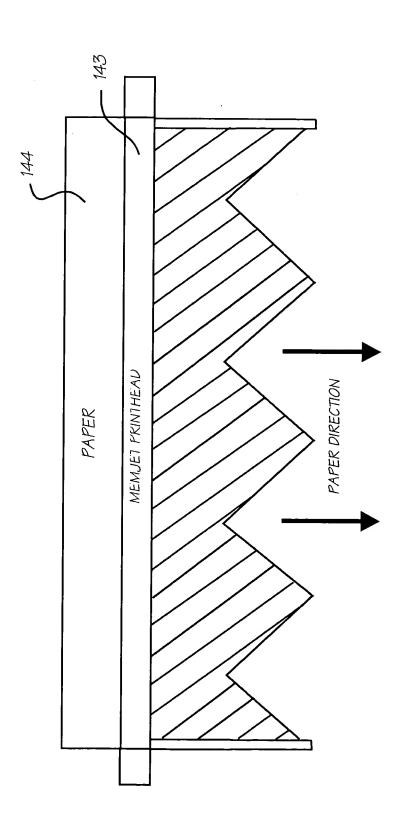


FIG. 29

ALL BLA TO FORM THE BOARD BLANDERS TO SEE THE REAL BLANDS BLANDS

FIG. 30



F1G. 3

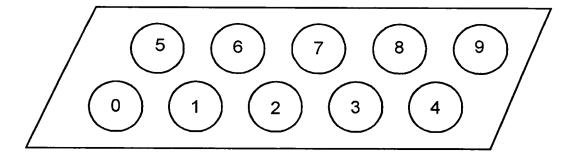


FIG. 32

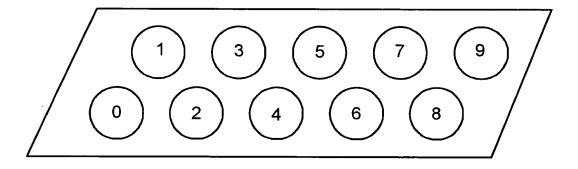


FIG. 33

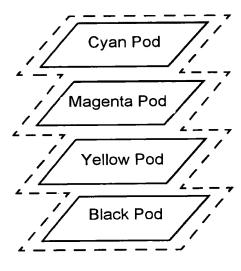


FIG. 34

0 1 2 3 4

// C / C / C / C / C /

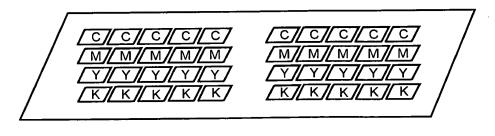
// M / M / M / M / M

// Y / Y / Y / Y / Y

// K / K / K / K

5 chromapods

FIG. 35



C = Cyan M = Magenta Y = Yellow

K = Black

FIG. 36

C = Cyan M = magenta Y = Yellow K = Black

FIG. 37

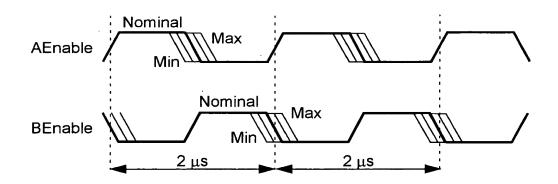


FIG. 38

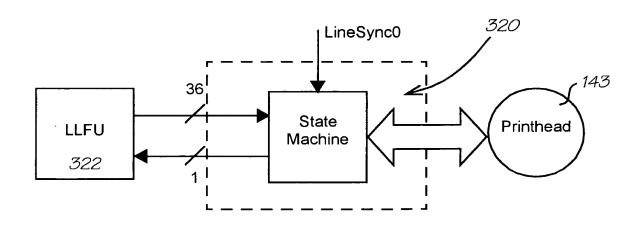


FIG. 40

TOTA AST

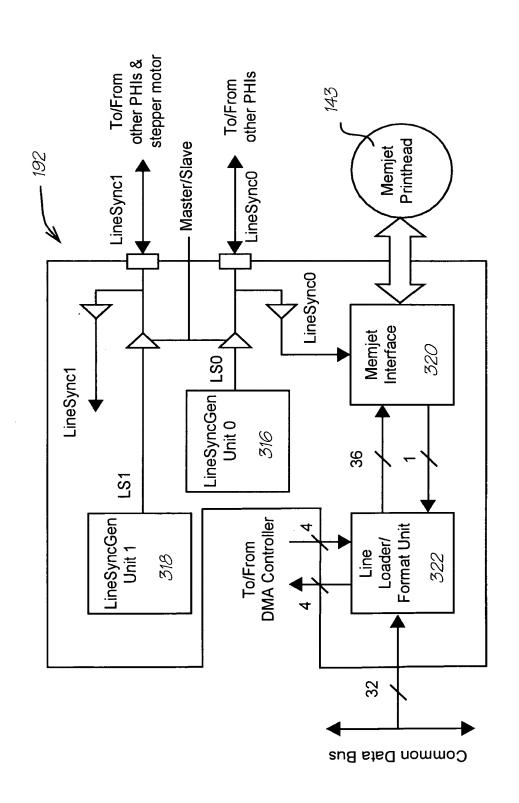
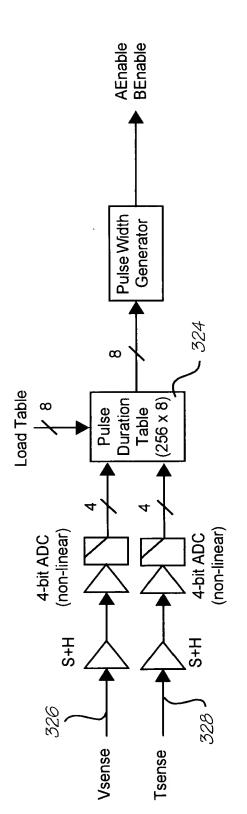


FIG. 39



ClearCount 32-bit Color1DotCount SRClock[1-4] √1 ▼Load 32 32 9 line to 4 line encoder တ Color1Data]

FIG. 42

L. P. Spire serve come come com

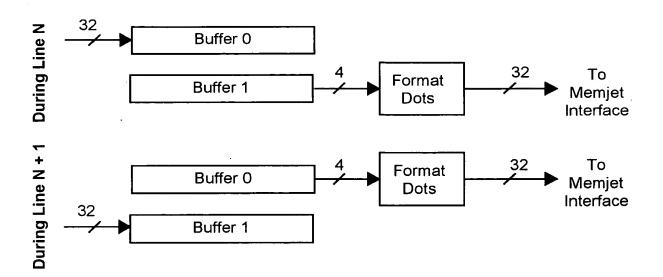


FIG. 43

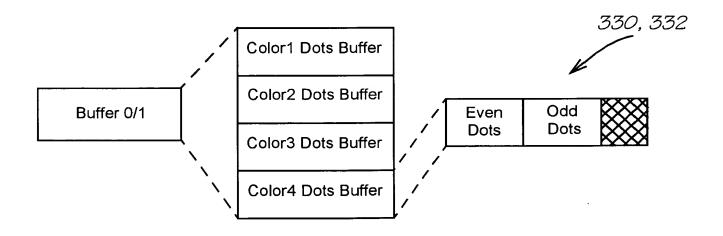


FIG. 45

LARLE BAN No. No. No. No. No. 10. No.

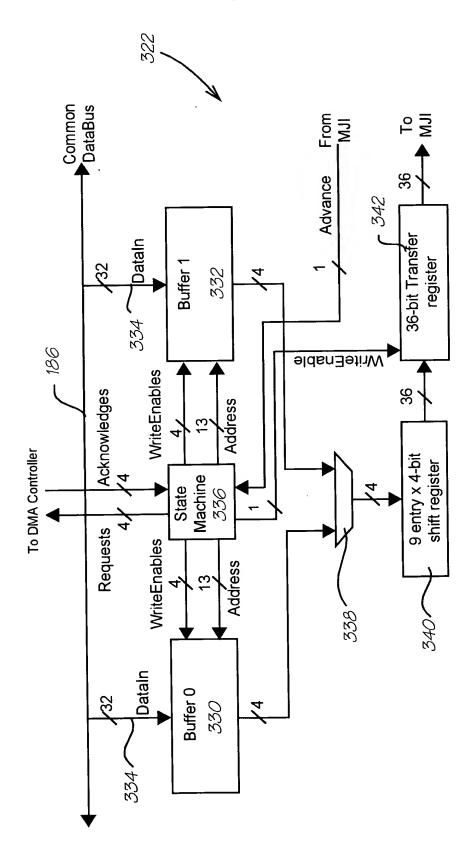


FIG. 44

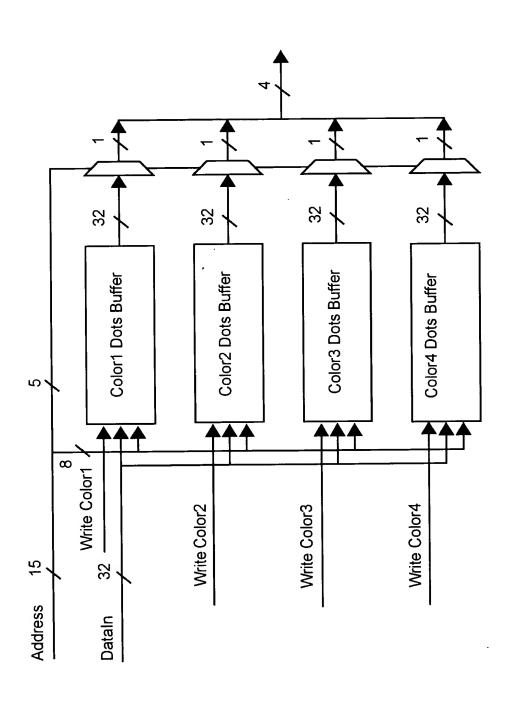


FIG. 46

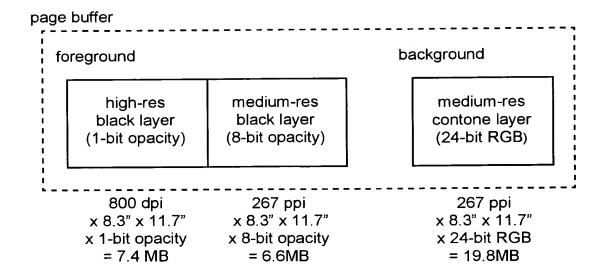


FIG. 47

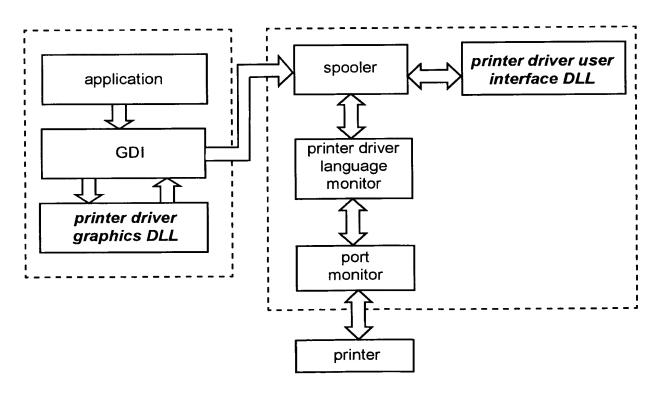


FIG. 48